

- Explain the coding format and different functions of UART.
- Explain the logic behind floating point Multiplication.

5. **Attempt any *one* part of the following:** **10 x 1 = 10**
- (a) What is Guarded signal assignment? Explain with suitable examples.
 - (b) What is the scan based design? Explain its testing sequence and general structure.
6. **Attempt any *one* part of the following:** **10 x 1 = 10**
- (a) What are the Multiple concurrent drivers? Explain different resolving methods.
 - (b) Explain the basic structure of Xilinx 4000 Series FPGA's.
7. **Attempt any *one* part of the following:** **10 x 1 = 10**
- (a) What do you mean by Testing? What are different issues related to design the test?
 - (b) Explain the various IEEE 754 floating point formats.